

ABSTRACT OF THE DISCLOSURE

A data communication system and method are provided to communicate in a multiprocessor interconnection network or other network. In one embodiment, the present system includes a number of logical circuits that are located in a number of nodes interconnected in a multiprocessor interconnection network. In this regard, the nodes include at least one source node, at least one destination node, and at least one intermediate node. The logical circuits include source logic located in the source nodes to identify data routes between respective source nodes and destination nodes through the one or more intermediate nodes. The data routes are specified by at least one destination port value and a current hop count that are attached to a data packet to be transmitted between respective source and destination nodes. The logical circuits also include routing logic in the intermediate nodes to route the data packets along the data routes, and destination logic in the destination node to detect a final destination of the data packet. The various embodiments of the present invention also include return routing logic in the intermediate nodes to record a return route in the data packet.

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